

Figure 1  
PRIOR ART

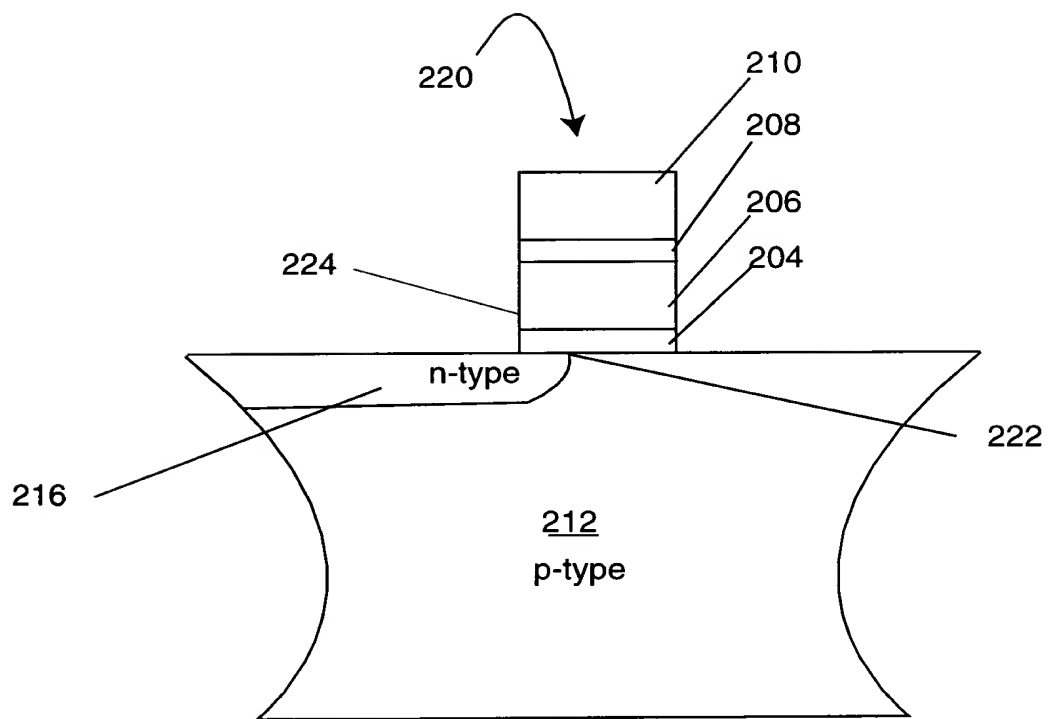


Figure 2  
PRIOR ART

A graph showing the voltage of the floating gate ( $V$  of floating gate) versus time for device 302. The vertical axis is labeled with  $-12.0\text{ V}$  and  $-6.0\text{ V}$ . The horizontal axis is labeled "time". The curve starts at  $-12.0\text{ V}$  and rises exponentially towards  $-6.0\text{ V}$ . A vertical line segment connects the curve to the label "302" at the top.

Figure 3  
PRIOR ART

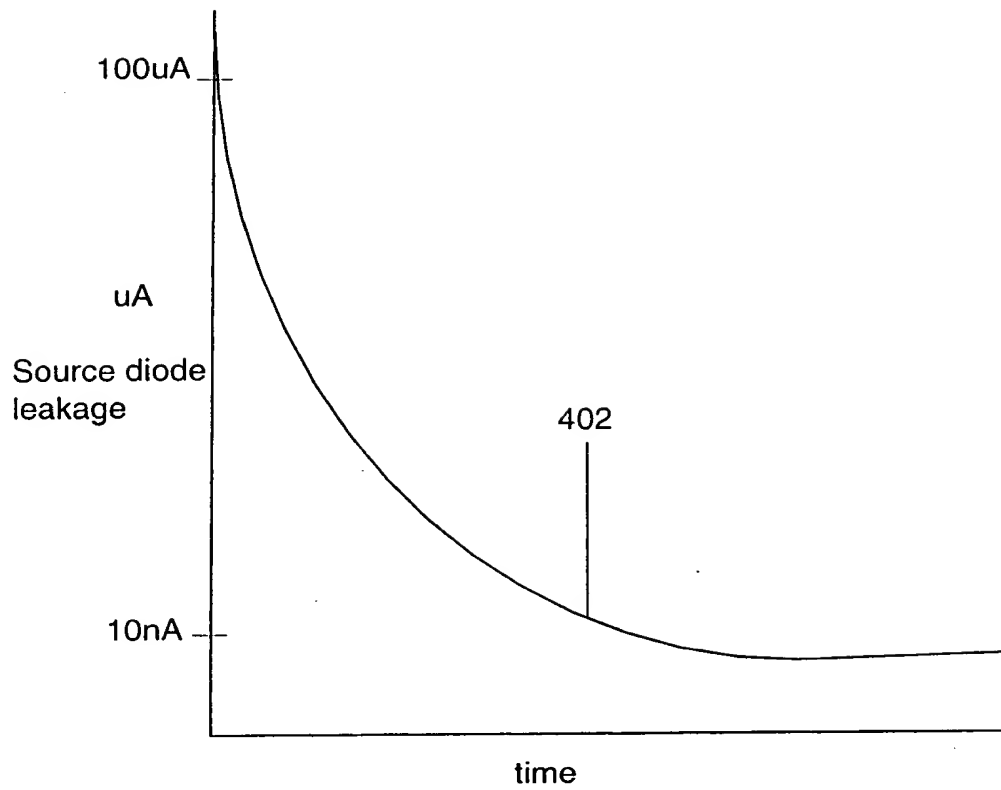


Figure 4

PRIOR ART

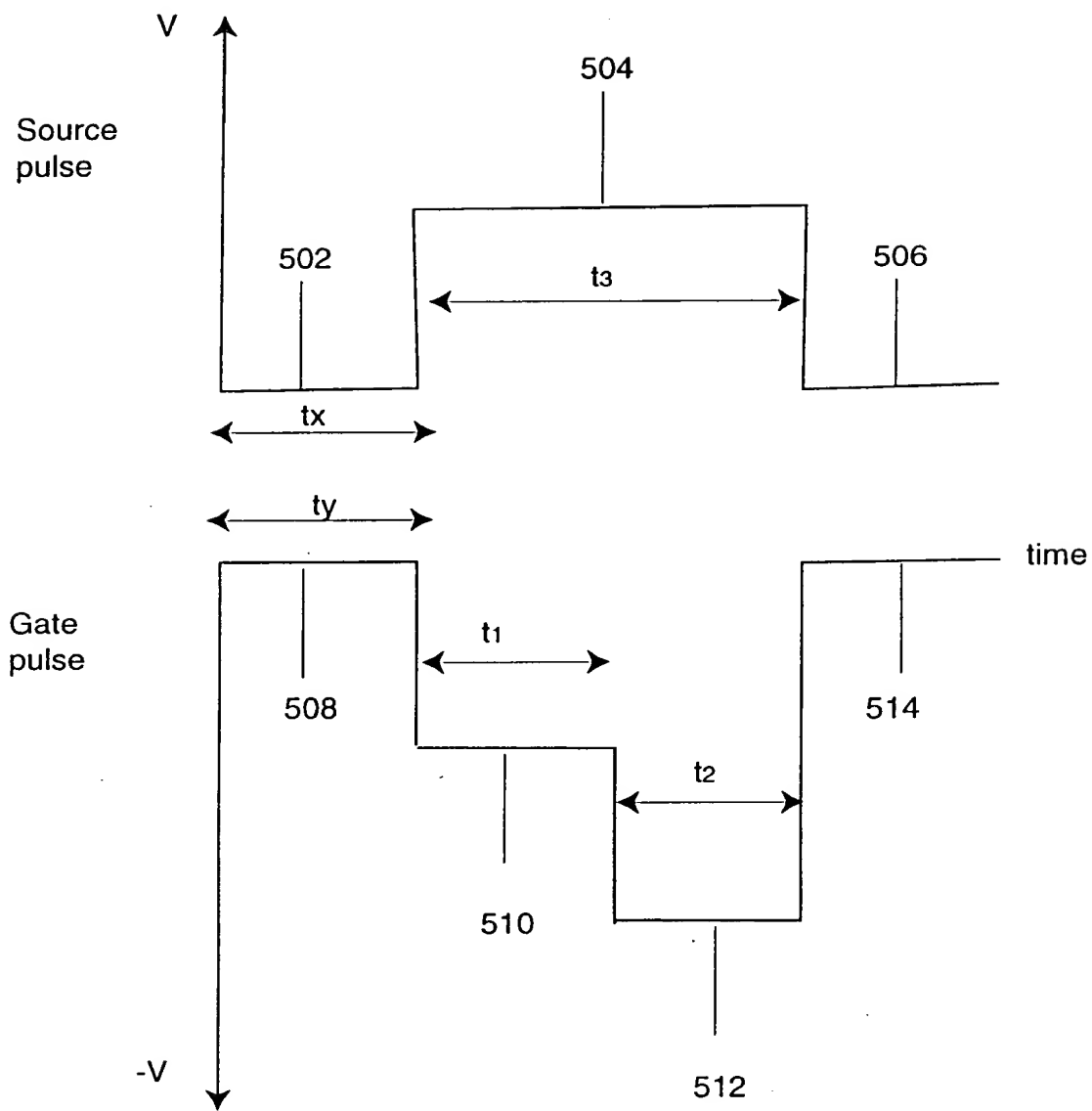


Figure 5

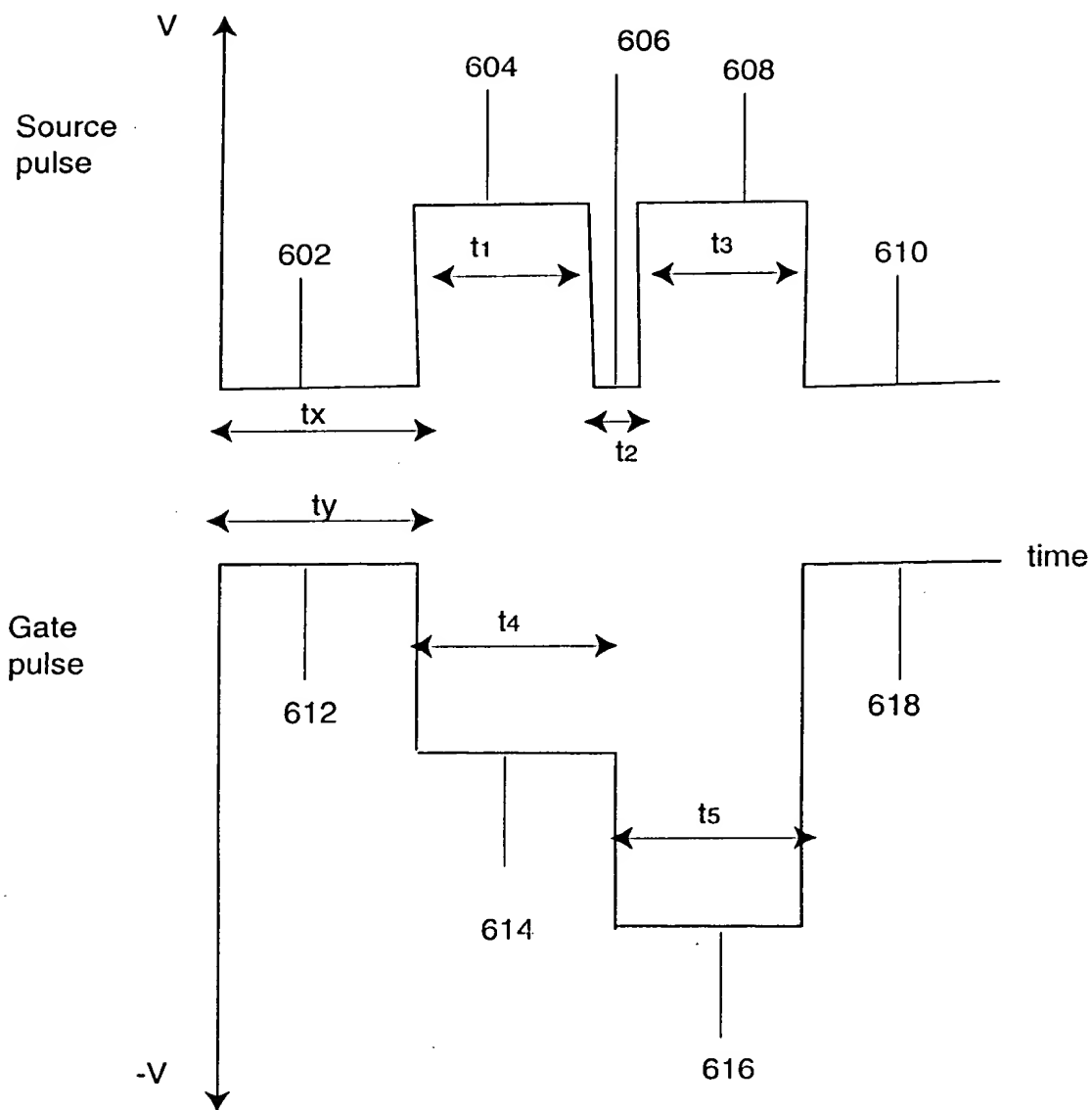


Figure 6

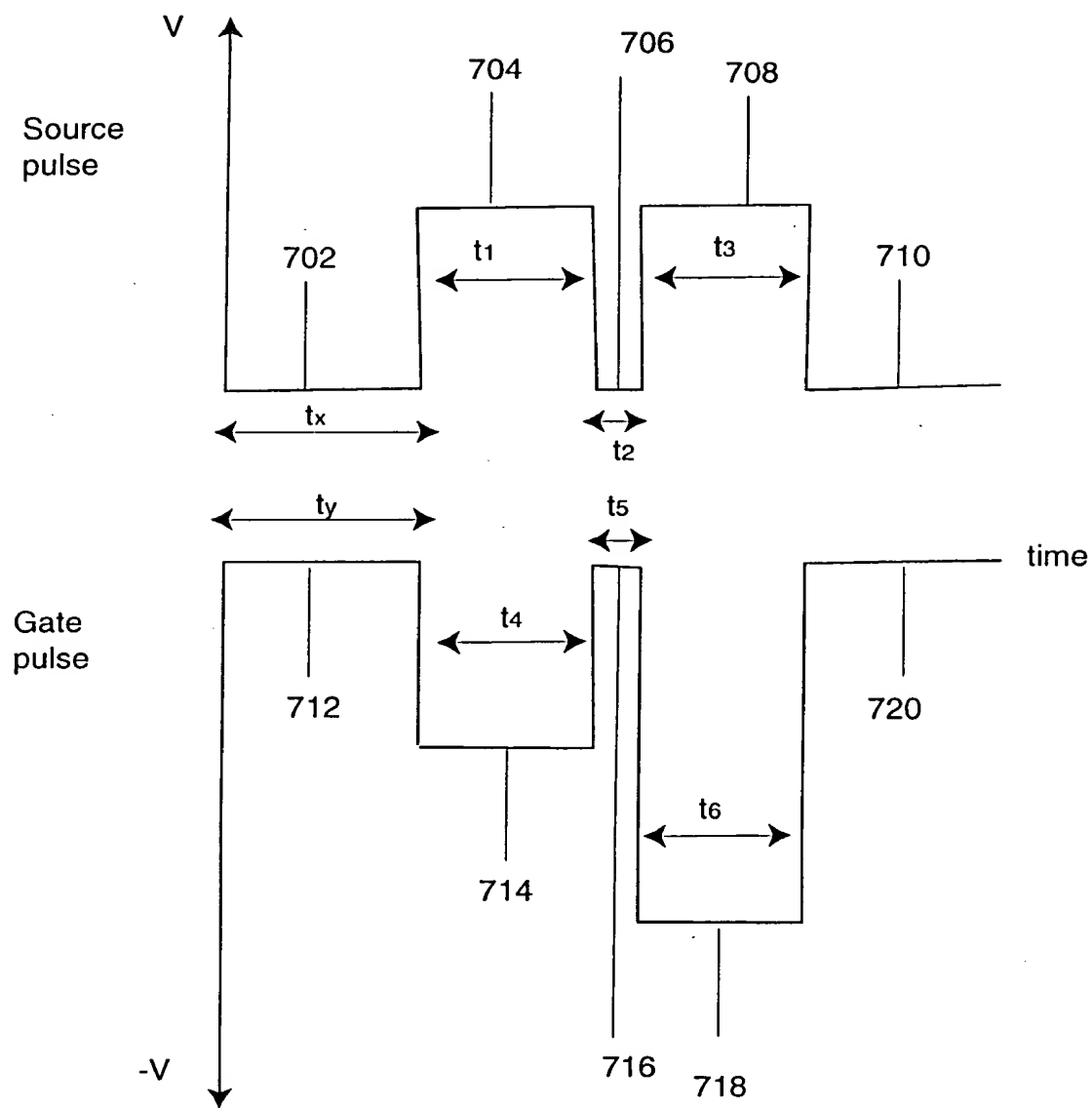


Figure 7

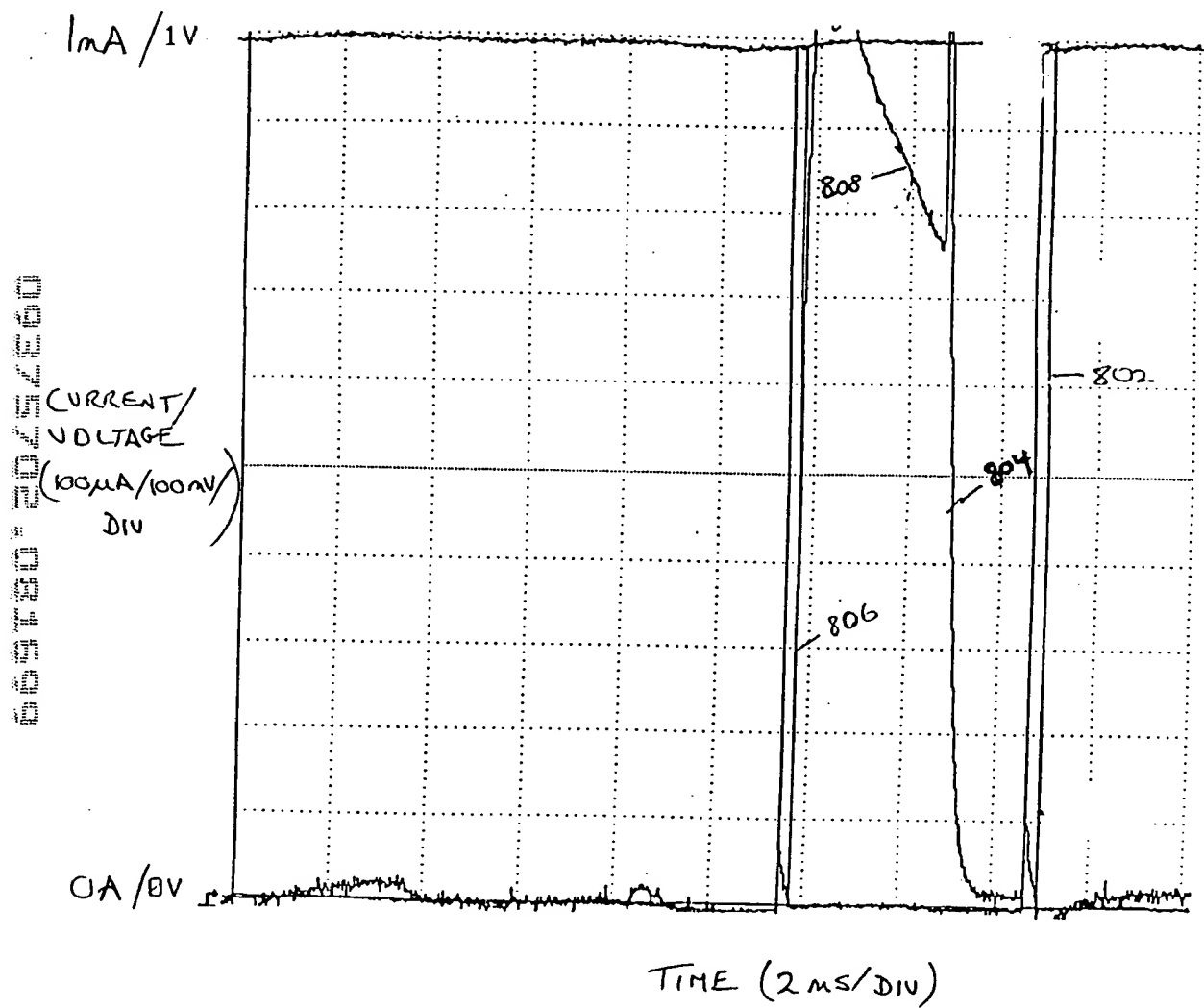


Figure 8 A



The diagram shows a cross-sectional view of a device 820. At the top is a stack of layers: a top electrode 832, a dielectric layer 830, and a bottom electrode 826. Below the bottom electrode 826 is a conductive layer 816. The conductive layer 816 is connected to ground through a resistor 840. The output of the device is connected to an oscilloscope 842. The capacitor 812 is formed by the bottom electrode 826 and the conductive layer 816. The conductive layer 816 is connected to ground through a resistor 840. The output of the device is connected to an oscilloscope 842.

Figure 8B

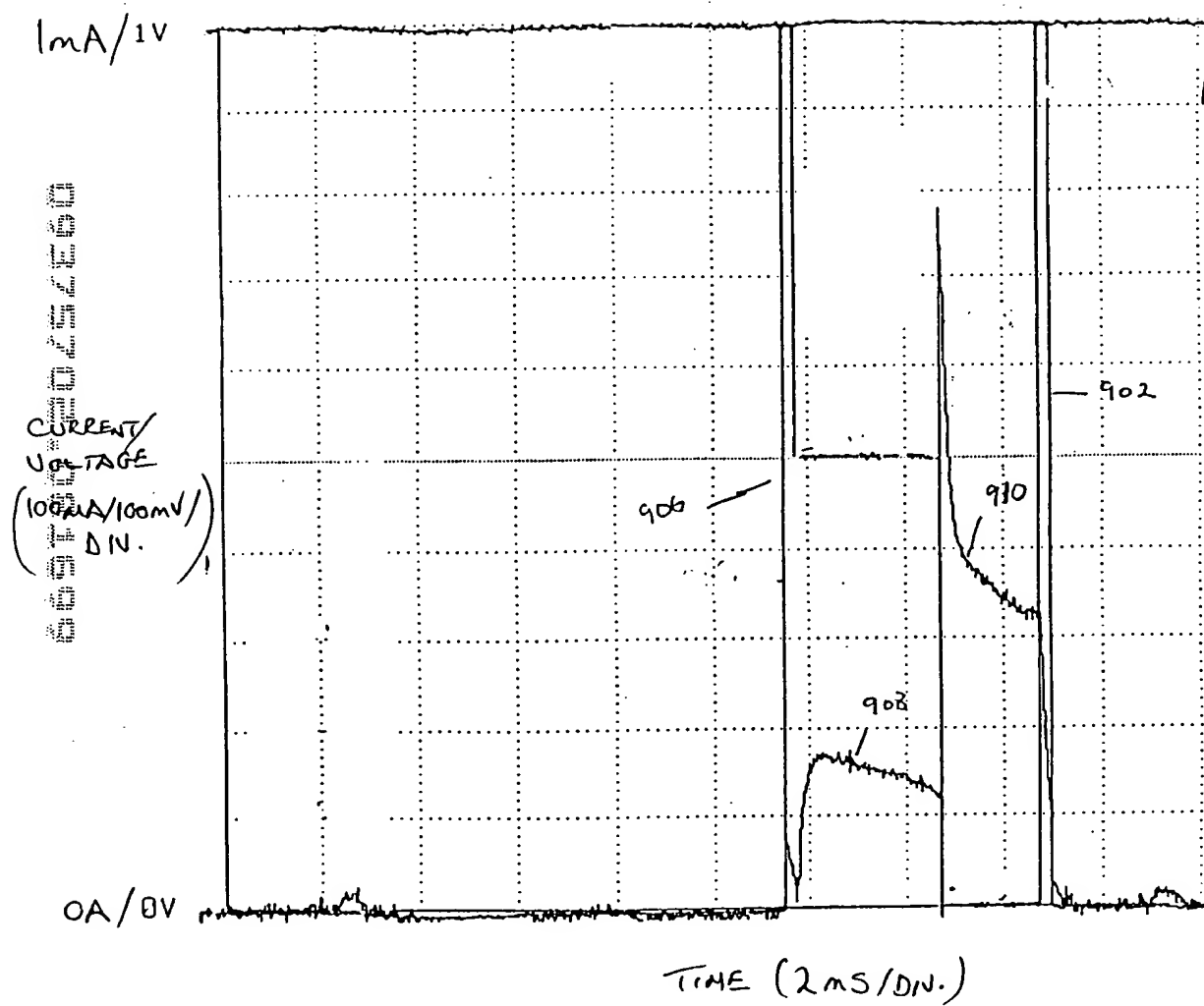


Figure 9